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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/043,516	01/11/2002	Johann Fuhrmann	DE 01007	8286

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EXAMINER

HU, SHOUXIANG

ART UNIT	PAPER NUMBER
2811	

DATE MAILED: 07/17/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/043,516	FUHRMANN ET AL.
	Examiner Shouxiang Hu	Art Unit 2811

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on ____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) Claim(s) ____ is/are allowed.
- 6) Claim(s) 1-15 is/are rejected.
- 7) Claim(s) ____ is/are objected to.
- 8) Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on ____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on ____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. ____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). ____ . |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____ . | 6) <input type="checkbox"/> Other: ____ . |

DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities:

The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

Arrangement of the Specification

As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.
- (d) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC (See 37 CFR 1.52(e)(5) and MPEP 608.05. Computer program listings (37 CFR 1.96(c)), "Sequence Listings" (37 CFR 1.821(c)), and tables having more than 50 pages of text are permitted to be submitted on compact discs.) or
REFERENCE TO A "MICROFICHE APPENDIX" (See MPEP § 608.05(a).
"Microfiche Appendices" were accepted by the Office until March 1, 2001.)
- (e) BACKGROUND OF THE INVENTION.
 - (1) Field of the Invention.
 - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (f) BRIEF SUMMARY OF THE INVENTION.
- (g) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (h) DETAILED DESCRIPTION OF THE INVENTION.
- (i) CLAIM OR CLAIMS (commencing on a separate sheet).
- (j) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).
- (k) SEQUENCE LISTING (See MPEP § 2424 and 37 CFR 1.821-1.825. A "Sequence Listing" is required on paper if the application discloses a nucleotide or amino acid sequence as defined in 37 CFR 1.821(a) and if the required "Sequence Listing" is not submitted as an electronic document on compact disc).

Claim Objections

2. Claims 1-15 are objected to because of the following informalities and/or defects:
3. Claims 1, 6, 7, 11 and 12 each recite broad recitations then recite the phrases starting with "particularly" that are respectively the narrower statements of the ranges/limitations, which is considered indefinite, since the resulting claims does not clearly set forth the metes and bounds of the patent protection desired. Note the explanation given by the Board of Patent Appeals and Interferences in *Ex parte Wu*, 10 USPQ2d 2031, 2033 (Bd. Pat. App. & Inter. 1989), as to where broad language is followed by "such as" and then narrow language. The Board stated that this can render a claim indefinite by raising a question or doubt as to whether the feature introduced by such language is (a) merely exemplary of the remainder of the claim, and therefore not required, or (b) a required feature of the claims. Note also, for example, the decisions of *Ex parte Steigewald*, 131 USPQ 74 (Bd. App. 1961); *Ex parte Hall*, 83 USPQ 38 (Bd. App. 1948); and *Ex parte Hasche*, 86 USPQ 481 (Bd. App. 1949).
4. In claim 9, the term of "ad" should read as: --as--.
5. In claim 10, the term of "are" should read as: --are/is--.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

7. Claims 1, 2 and 6-15, as being best understood in view of the above claim objections, are rejected under 35 U.S.C. 102(b) as being anticipated by Hierold et al. ("Hierold"; DE 19738990A1; of record).

Hierold discloses an electric or electronic circuit arrangement and a method of protecting a electronic circuit arrangement thereof (Figs. 1, 2, 5 and 7, particularly Fig. 5), comprising: a layered carrier substrate (2) of a semiconducting or insulating material; at least one integrated circuit constituted by at least two spaced and lithographically applied conductor tracks (20₁, see Fig. 5 in view of Fig. 1) on the carrier substrate (2); at least one dielectric shielding layer (all insulating layers above the substrate 2) including an inherent insulation layer and/or passivation layer (the layer between the substrate 2 and the layer 40) and/or a further protective layer (the insulating layer(s) above the layer 40) situated between the conductor tracks (20₁) and/or laterally with respect to the conductor tracks (20₁) and/or on the conductor tracks (20₁), provided for protecting the integrated circuit from external influences so that the integrated circuit has a specific, particularly lateral and/or particularly parasitic capacitance (C) determined by the dielectric shielding layer, characterized in that at least one signal-generating unit (51) including at least an oscillator unit is connected to the contact terminals (see Fig. 5) of the integrated circuit, the output frequency (f) of which unit is substantially determined by the specific capacitance, in that the signal-generating unit (51) precedes at least a first counting unit (521) which is clocked at the output frequency (f) of the signal-generating unit (51), in which counting unit an actual value count can be determined

after a predetermined temporal counting period, in that at least a second counting unit (53) clocked at a reference frequency is provided, in which counting unit a nominal value count can be determined after the predetermined temporal counting period, in that the first counting unit (51) and the second counting unit (53) precede at least one comparator unit (54) for comparing the actual value count with the nominal value count, while the functions of the integrated circuit can be blocked and/or locked and/or interrupted temporarily or permanently in the case of an error indication which occurs when the actual value count is compared with the nominal value count.

Regarding claim 2, the conductor tracks (20₁) in Hierold are at least sectionally arranged parallel to each other and/or in a meandering intermeshing configuration (see Fig. 5).

Regarding claim 6, it is noted that the signal-generating unit (51) in Hierold includes an oscillator, which normally inherently comprises at least one oscillator circuit consisting of at least one capacitive unit including a capacitor, and at least one resistive unit including a resistor, and/or at least one oscillator circuit consisting of at least one capacitive unit including a capacitor, and at least one inductive unit including a coil.

Regarding claims 7 and 13, a differential evaluation unit is naturally constituted by the first counting unit, the second counting unit and the comparator unit in Hierold.

Regarding claim 10, the arrangement in Hierold further includes a coding unit (522), which manifests that the first counting unit and/or the second counting unit are/is naturally formed on a digital basis.

Regarding claim 11, the arrangement of Hierold is for a chip card or smart card.

Claim Rejections - 35 USC § 103

8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

9. Claims 3-5, as being best understood in view of the above claim objections, are rejected under 35 U.S.C. 103(a) as being unpatentable over Hierold et al. ("Hierold"; DE 19738990A1; of record).

The disclosure of Hierold is discussed as applied to claims 1, 2 and 6-15 above.

Regarding claim 3, it is noted that the mutual distance between the conductor tracks is in an art-recognized result-effective parameter of importance subject to routine experimentation and optimization; and that the recited micrometer range is well within the art-recognized normal range for the mutual distance between capacitor electrodes.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to make the circuit arrangement of Hierold with the distance between the conductor tracks being in the micrometer range, so that a circuit arrangement with optimized performance would be obtained, because it has been held that "[W]here the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation." In re Aller, 220 F.2d 454, 456, 105 USPQ 233, 235 (CCPA 1955).

Regarding claims 4 and 5, it is noted that silicon oxide and opaque resin are each among art-recognized insulating passivation materials commonly used for reliably

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and/or invisibly protecting IC devices, as evidenced in the prior art such as Daum (US 5,821,582; see col. 3, lines 32-33) and/or Ban et al. (US 6,060,773; see col. 1, lines 22-24).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to make the circuit arrangement of Hierold with the dielectric shielding layer being formed of silicon oxide or opaque resin, so that an IC device with reliable and/or invisible protection would be obtained.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References A-D are cited as being related to a card protection arrangement.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is (703) 306-5729. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

SH

July 13, 2003



Shouxiang Hu
Patent Examiner
TC2800